

A cross-sectional view of a semiconductor device 50. The device features a substrate 51 with a central layer 52 and side layers 53. A central rectangular block 54 is positioned on top of layer 52, with a smaller block 54a on its top surface. Two large, rounded structures 55 are situated on either side of block 54, extending from the substrate 51. The top surface of the device is labeled 56. The side surfaces of the substrate are labeled 53a and 53b. The bottom surface of the substrate is labeled 56b. The entire structure is labeled 50 with a downward-pointing arrow.

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